Pin Configuration..

Note The OUTx port has fast turn-

Resistor (LDSW_DIS Register), “0”: Disable. “1”: Enable.

Write Reg start address byte = cmdadr(x + REG’s 7bit address)

0x07 LP3W50 3EC Register ---- [redacted]

				Note: The bits will always clear immediately when written to and always read back 00.
3	seq_on	0	R	Indicates the activation signal of SEQ. 0b: Indicates that the sequencing is not in process 1b: Indicates that the sequencing is executing and somewhere between the start of slot 1 and the end of slot 7. The bit remains a 1 until slot 7 has completed at start-up or slot 1 has finished at shutdown, regardless of what slots are used.

2:0 seq_cnt[2:0]

t_{DOFF} Turn-