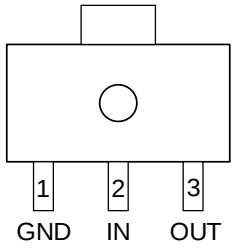
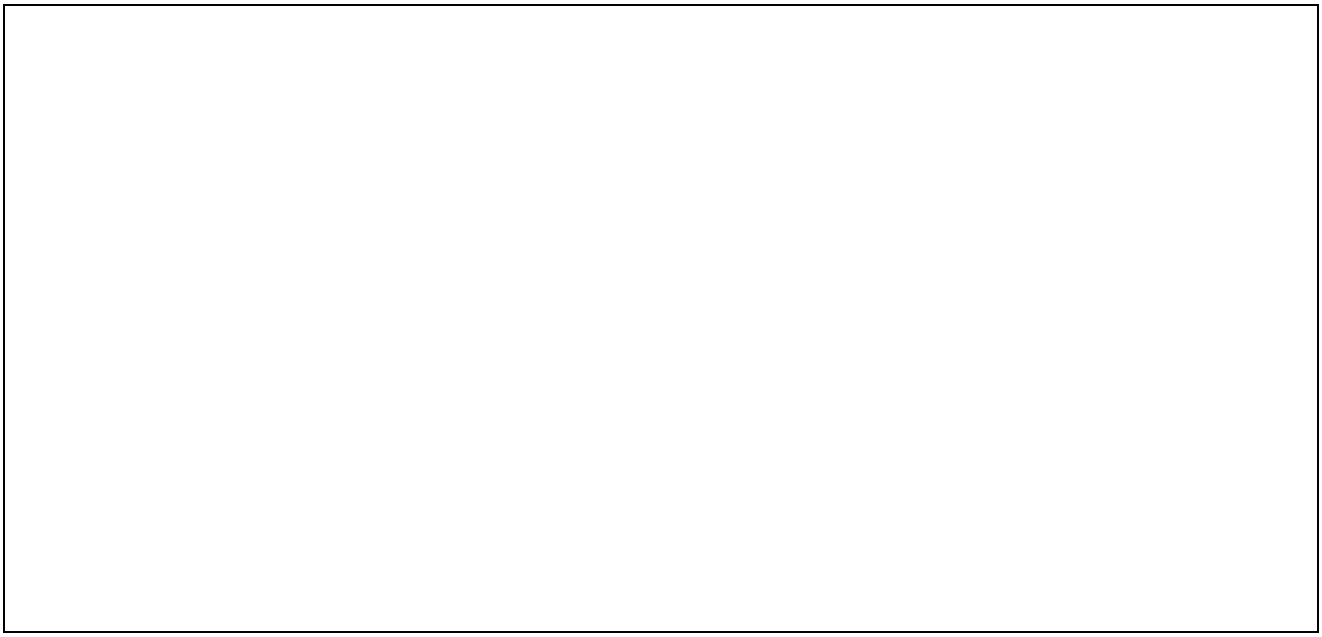
Pin Configuration



Block Diagram



Functional Description

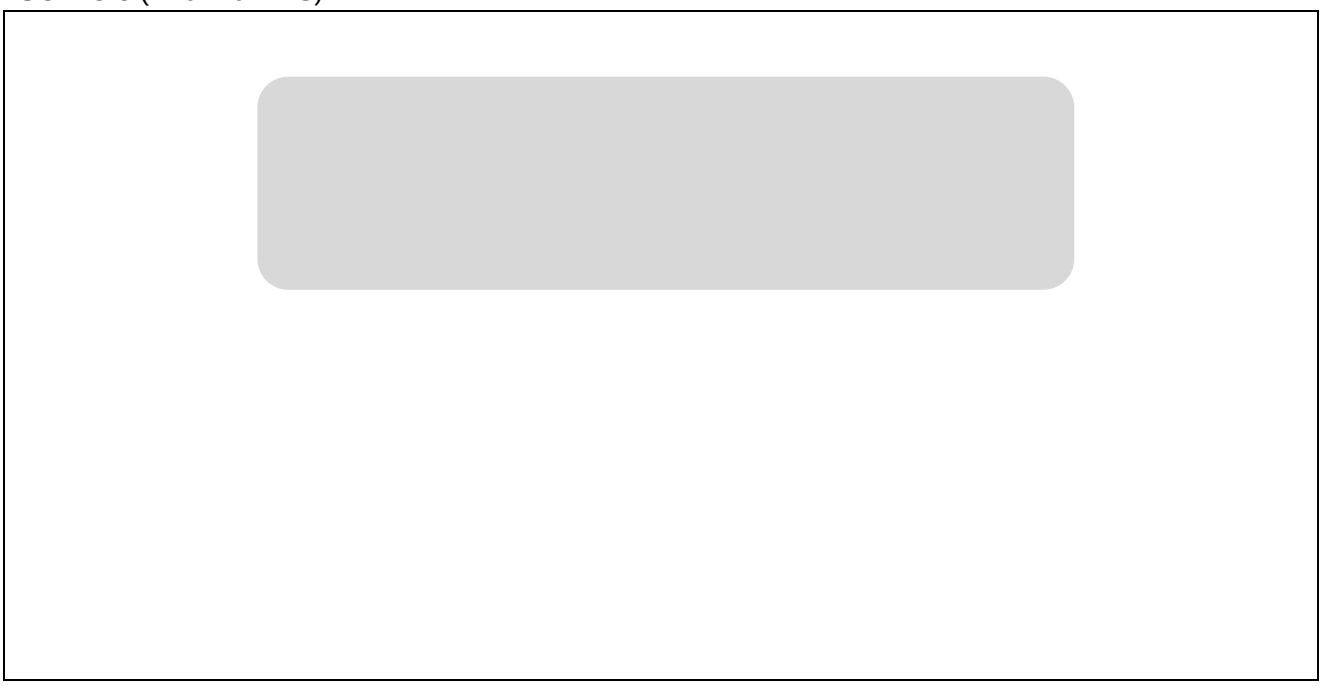
Input Capacitor

It is recommended to connect a 1εF capacitor between the IN and GND pins to eliminate input power fluctuations and reduce output ripple. This input capacitor must be as close to the chip as possible to ensure stab88.49 Tm0 G[s)-5(tab)4(88.49 Tm0 G[s)-5(tab)4(88.49 Tm0 G[s)-5(tab)4(88.49 T)-20(r)34BDC f]TJETQ EMC 9ETo21

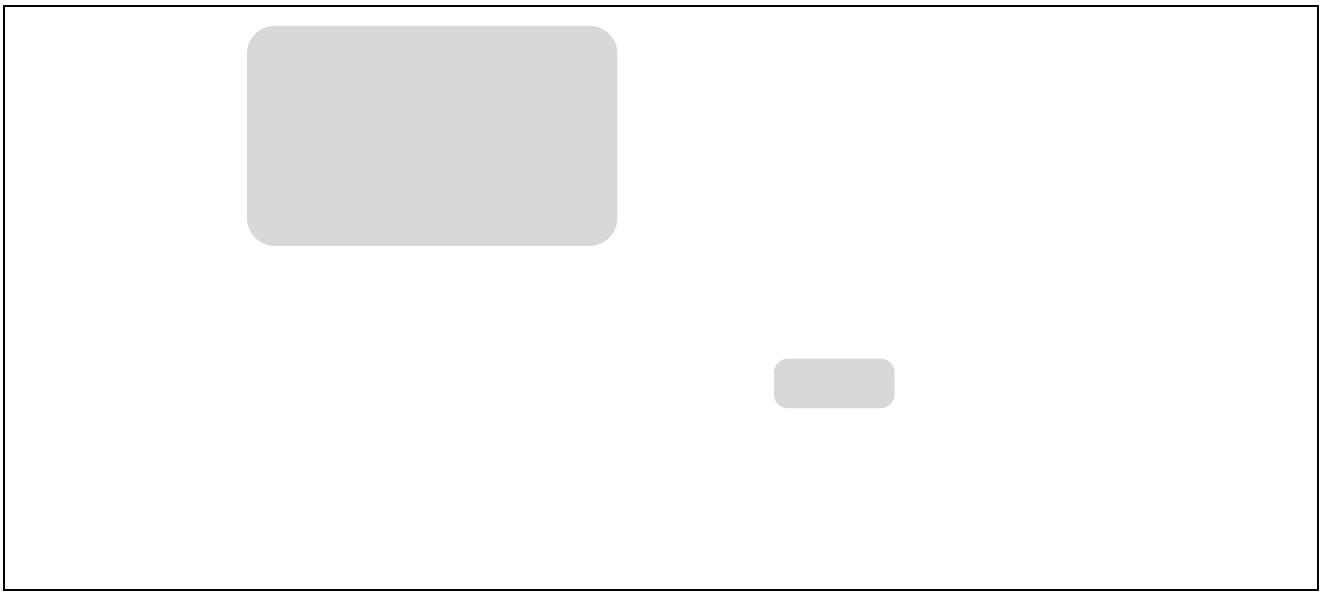
Thermal Shutdown

ET6

SOT23-3 (ET61H61XXS)



SOT23-5 (ET61H61XXT)



Package Dimension

SOT89-3

Unit: mm